



PATENT
Docket No. 29287/126

2154
Supplemental
Pre-Amendment
#6
T.D.
03/12/03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Kiyokazu NISHIOKA *et al.*

Serial No. : 10/053,683

Filed: 24 January 2002

For: PROCESSOR

Group Art Unit : 2154

Conf. No. 1063

Examiner : (Unknown)

RECEIVED

FEB 19 2003

Technology Center 2100

SECOND SUPPLEMENTAL PRELIMINARY AMENDMENT

COMMISSIONER FOR PATENTS
Washington, D.C. 20231

Sir:

Some errors were discovered during proofreading of the parent patent no. 6,401,190 B1 which should be corrected. It is therefore respectfully requested that the specification be amended as follows:

In the Specification:

Page 2: Amend the first full paragraph starting at line 5 as follows:

The former can be achieved comparatively simply but increases the difficulty of packaging design, resulting in increased cost. To implement the performance at a reasonable cost, the latter approach may also be necessary. However, the parallel computing approach presents problems of whether applications are ready for parallelism and that control for effective use of a plurality of computing units is complicated. As for applications, a fairly high parallelism is found as long as multimedia are concerned. For example, 8 computational operations is are concurrently executable in motion picture compression.